

ART UNIT: 2813

EXAMINER: David I. Hogans

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
CINDY SEIBEL et. al )  
Serial No.: 09/941,817 )  
Filed: Aug. 29, 2001 )

**AMENDMENT B DETAILS**

For: **METHOD FOR SEMICONDUCTOR GATE DOPING**

**In the Claims:**

Amend claims 1 and 11 as follows:

1. (Amended) A method of forming a doped polycrystalline silicon gate in a Metal Oxide Semiconductor (MOS) device formed on a top surface of a crystalline silicon substrate comprising:

a) forming an insulation layer on said top surface of the silicon substrate;  
b) forming an amorphous silicon layer on top of and in contact with said insulation layer;

c) introducing a dopant in a top surface layer of said amorphous silicon layer;  
and

d) irradiating said top surface layer of said amorphous silicon layer with a radiation beam to heat said top surface layer **[and] to heat said top surface layer to a temperature between 1150°C and the melting temperature of said silicon substrate to** initiate explosive recrystallization of said amorphous silicon layer



#12/ExH2  
B

PATENT  
Attorney's Docket No. 3521.150

5/8/03

*John H.*

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**RESPONSE TO EXAMINER'S  
SECOND ACTION AND  
AMENDMENT B**

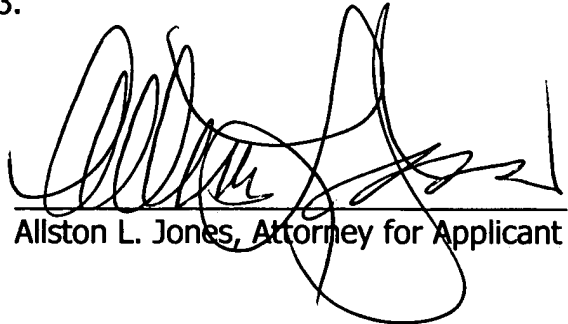
For: **METHOD FOR SEMICONDUCTOR GATE DOPING**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**EXPRESS MAIL CERTIFICATE**

I hereby certify that this paper, and the listed enclosures, and fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated below and is addressed to, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 with the Express Mail Label No. EV248041367US on May 1, 2003.

  
Allston L. Jones, Attorney for Applicant

Dear Sir:

The following Amendment and Response is being submitted in response to the